



Performance Related Properties of Electroceramics

Technical Work Area 24

Call for Participation

Project International Intercomparison of Highly Accelerated Life Test for Multilayered Ceramic

Objectives

Objectives of proposed project is to establish a testing and analysing method to predict the life time of high-capacitance multi-layered ceramic capacitors (MLCC) from the result of Highly Accelerated Life Test (HALT) at high temperatures and high voltages

Background

MLCCs are one of the most used passive components in consumer electronics. Among the various properties of MLCCs, reliability is one of the crucial parameters in the design of electronic components. The HALT is commonly used as a test for the reliability of MLCCs.

Standardization Needs

At present, the procedure of HALT is not specified, which means each company uses its own way for HALT and it is difficult to compare the result of HALT obtained from different manufacturers. Therefore, the establishment of precisely determined test conditions and analysis procedure of HALT are required.

Work Programme

It is planned to organize a VAMAS interlaboratory round-robin test (RRT) to verify measurement protocols and analysis methods. The timescale is as follows,

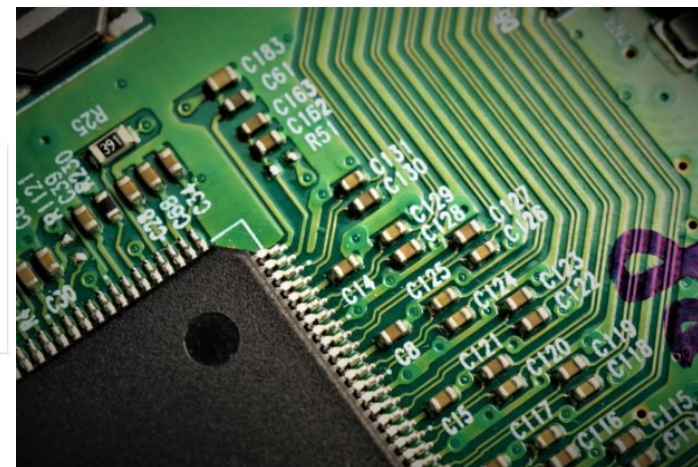
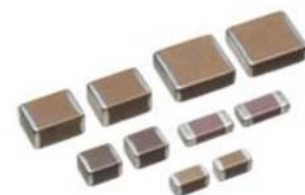
From June to July 2019:
Test samples and the draft protocol of HALT delivery.

From August to December 2019 :
RRTs and measurement data collection.

From January to March 2020
Finish the documentation of the procedure of HALT.

Deliverables and Dissemination

The expected end products of the proposed studies are pre-standardised procedures and guidelines for the execution of measurements and analyses. The reports, publications and presentations on the developed subjects will be also publicized. The interlaboratory study will be disseminated in a peer-reviewed scientific journal and used to develop a new standard in IEC/TC40.



Funding

Participants fund their own involvement the project. Materials for the interlaboratory comparison will be supplied by Murata manufacturer.

Status

Samples for the interlaboratory RRT will be despatched until the end of August 2019. Participants will be expected to report the results by March 2020.

References

- IEC 60384-22: Fixed capacitors for use in electronic equipment Part 22: Sectional specification Fixed surface mount multilayer capacitors of ceramic dielectric.

For more information on participation, please contact:

Project Leader

Prof. Takaaki Tsurumi
Tokyo Institute of Technology, Japan
Email: ttsurumi@ceram.titech.ac.jp

TWA Chair

Chair, VAMAS TWA 24
Prof. Takaaki Tsurumi
Tokyo Institute of Technology, Japan
Email: ttsurumi@ceram.titech.ac.jp

www.vamas.org