

# USER AREAL DENSITY OPTIMIZATION FOR CONVENTIONAL AND 2D DETECTORS/DECODERS

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## I. INTRODUCTION

Advances in the magnetic recording channel have the potential to detect and decode the stored data at lower SNRs, higher channel areal densities (CAD), lower code rates (R) and lower bit aspect ratios (BAR) than conventionally used in today's systems. The magnetic recording channel today optimizes at parameters in the vicinity of  $R = 0.85$ ,  $BAR = 3$ ,  $BL = 12.5\text{nm}$ ,  $TP = 38\text{nm}$ , but it has been suggested in [1] that as the industry moves to shingled recording and starts using heads with multiple readers, the density could optimize at lower BAR's and code rates, and at a higher CADs. In the current work, we use the grain-flipping probability (GFP) model developed in [2], [3] together with conventional and advanced recording channels to determine the optimized user areal density (UAD) that can be delivered to the customer using conventional and advanced 2D detectors and decoders.

## II. GFP DATA GENERATION MODEL

In this work, the GFP model is used to generate mass waveforms at various CADs, UADs, code rates (R) and BARs. In this study, we attempt to maximize the UAD over R and BAR. The metric that is observed is the frame error rate (FER) of the coded channel as a function of the UAD. The GFP model is trained from micromagnetic simulations. In this work, we started with a micromagnetic parameter set similar to that in [1] in which the target density is 4Tbps, but it was agreed that we would scale them down by  $\sqrt{2}$  to a target density of 2Tbps, which is more appropriate given the state of today's head and media technology. The head and media parameters used in the micromagnetic simulations are shown below in Fig. 1.

Parameter	Value	Parameter	Value
Grain density	(Tgpsi) 11.4	$\sigma_{gs}$	20%
Grain boundary	(nm) 1.4	$\sigma_{Ku}$	5%
$K_u$	(J/m <sup>3</sup> ) 450e3	$\sigma_{Ms}$	0%
$M_s$	(A/m) 750e3	$\sigma_{Hk}$	5%
$H_k$	(kOe) 12	$\sigma_{Ax}$	3%
exchange	(erg/cm <sup>2</sup> ) 2.5	$\sigma_{ez}$	3°
$A_x$	(J/m) 5.6e-12	HMS (nm)	5.6
$e_z$	0°		
media thickness	(nm) 14.1		

Parameter	Symbol	Value	Parameter	Symbol	Value
Main Pole trailing width	MPTW	70nm	Side shield gap	SSG	14nm
Main Pole height	MPH	130nm	Trailing shield gap	TSG	14nm
Main Pole angle	MPA	75°	Head Media spacing	HMS	5.6nm
Interlayer thickness	ILt	1.4nm	Media thickness	Mt	14nm

Fig. 1. Media parameters for the micromagnetic simulations are shown on the left, while the head parameters are on the right.

The micromagnetic simulations are used to characterize a GFP look-up table (LUT) which is then used to generate signals for simulations using conventional 1D LDPC coded channels and the FER is measured. The UADs, code rates and BAR's are the independent variables in our simulations. The UAD was varied from 1.35 to 2.2 Tbps in steps of 0.05, the code rate R was varied from 0.5 to 0.95 in steps of 0.05 while the BAR was varied from 1.0 to 3.1 in steps of 0.3. Results with the conventional 1D channel at representative BAR's are shown in Fig. 2 below.

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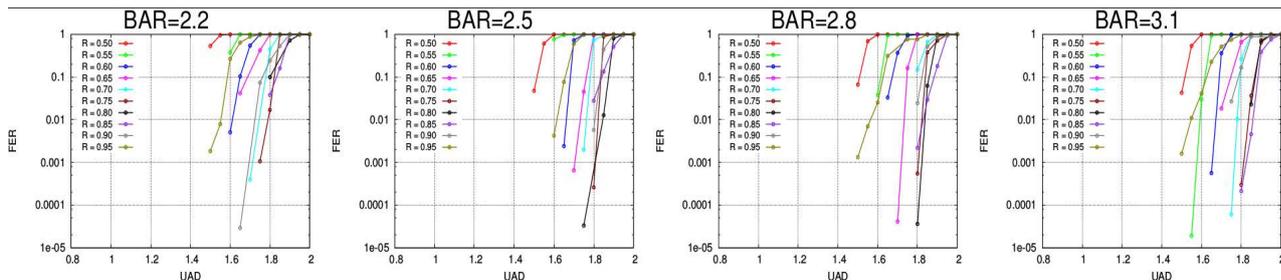


Fig. 2. FER vs UAD at different BARs and code rates.

The results from Fig. 2 show that for a conventional channel, the performance improves up to BAR=3.1 and an optimum code rate of 0.8 to 0.85 is observed at this BAR, with an achieved UAD of around 1.8 Tbps. It is noted here that the head/media are spec'd at 2Tbps. These results match closely with the HDD system parameters today.

### III. 2D SIGNAL PROCESSING AND CODING SYSTEM

The system shown in Fig. 3 employs a three-input one-output 2D MMSE linear equalizer of size  $3 \times 15$ , for estimation of the coded data bits on the central track. This setup assumes a read head capable of simultaneously reading three tracks. The MMSE equalizer reshapes the channel to an 8+16D 1D PR target on the central track; processing three rows at a time enables equalization of the ITI from the outer two tracks. The equalizer output flows into a two-state 1D BCJR detector that uses the PR target to compute branch labels. The BCJR exchanges LLRs with an irregular repeat-accumulate (IRA) LDPC decoder to minimize the user BER. The main innovation is the use of three tracks to estimate the central track, within a relatively low-complexity system architecture.

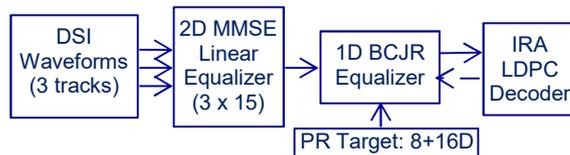


Fig. 3. 3-input / 1output MMSE/BCJR turbo detector

### IV. SIMULATION RESULTS

Fig. 4 presents BER vs. user bits per grain for the system shown in Fig. 3. For a fixed BL of 11 nm, the best result of 2.289 Tbps is achieved at 21 nm track pitch (1.91 BAR). This is somewhat higher than the best results for the baseline one-track system in Fig. 2, which achieves 1.80 Tbps; the improvement is due to processing three tracks at once.

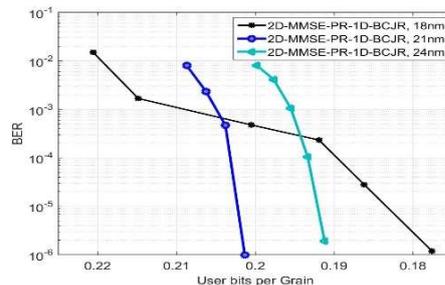


Fig. 4. Simulation results for the system in Fig. 3

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